AMENDMENTS TO THE CLAIMS

Listing of the Claims:

 (Currently amended) A method for notifying a user device coupled to an integrated services hub that communication has been terminated with a remote device comprising:

receiving a disconnect signal from the remote device into the integrated services hub;

determining the status of the user device; and

when the user device is off-hook, relaying a call termination notification signal to the user device from the integrated services hub via a user device interface coupled to the user device;

wherein the call termination notification signal comprises temporarily placing the user device interface in a disabled state.

2. (Cancelled)

- (Currently amended) The method of claim 2-1 wherein the disconnect signal is received from the remote device by a network interface within the integrated services hub.
- (Original) The method of claim 3 wherein the network interface is a WAN interface.

- (Original) The method of claim 3 wherein the network interface sends the disconnect signal to a CPU within the integrated services hub.
- (Original) The method of claim 5 wherein the CPU determines the user device to which the disconnect signal pertains.
- (Original) The method of claim 6 wherein the call termination notification signal is sent from the CPU to the user device interface coupled to the user device.
- 8. (Original) The method of claim 7 wherein the user device interface is a SLIC.
- 9. (Original) The method of claim 8 wherein the call termination notification signal sent to the SLIC indicates to the user device that the user device should enter an on-hook status.
- (Original) The method of claim 9 wherein the CPU places the SLIC in a standby state upon the user device entering an on-hook status.
- 11. (Original) The method of claim 10 further comprising:

detecting the status of the user device by the CPU after sending the call termination notification signal to the SLIC; and

when the user device is in an off-hook status, sending another call termination notification signal to the SLIC.

12. (Currently amended) A method for defining the status of a user device upon termination of communication with a remote device comprising:

receiving a disconnect signal from the remote device;

determining the status of the user device via a CPU monitoring a user device interface coupled to the user device;

the user device interface;

when the user device is off-hook, sending a call termination notification signal to

wherein the call termination notification signal comprises temporarily placing the user device interface in a disabled state;

interpreting the call termination notification signal by the user device as notification for the user device to enter an on-hook status; and

placing the user device interface in a standby state upon the placement of the user device in an on-hook status.

13. (Original) The method of claim 12 wherein the user device interface is a SLIC.

14. (Currently amended) An apparatus for notifying a user device coupled thereto that communication has been terminated with a remote device comprising:

a network interface configured for receiving a disconnect signal from the remote device into the apparatus:

a CPU coupled to the network interface and configured for determining the status of the user device; and

a user device interface coupled to the user device and the CPU and configured for relaying a call termination notification signal from the CPU to the user device when the user device is off-hook;

wherein the call termination notification signal comprises temporarily placing the user device interface in a disabled state.

- (Original) The apparatus of claim 14 wherein the apparatus is an integrated services hub.
- 16. (Original) The apparatus of claim 15 wherein the network interface is a WAN interface.
- (Original) The apparatus of claim 16 wherein the user device interface is a SLIC.

18. (Original) The apparatus of claim 17 further comprising a SLAC coupled to the SLIC and the CPU, the SLAC having an internal register for indicating the state of the SLIC, the internal register being accessible by the CPU.